Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1	1. (Currently Amended) A Fibre Channel device for use in a Fibre Channel fabric,
2	the fabric coupling a plurality of external data devices, the fabric configured into at least
3	two zones, where the-external data devices are allowed to exchange data packets only
4	with external data devices in the same zone, the Fibre Channel device enforcing the $\underline{a}\underline{t}$
5	least two zones in hardware, the Fibre Channel device comprising:
6	a receiving port for coupling to the fabric and receiving data packets;
7	a first transmitting port for coupling to the fabric and transmitting data packets;
8	a second transmitting port for coupling to an external data packet processing
9	device; and
10	device logic connecting said receiving port and said first and second transmitting
11	ports, wherein said device logic includes:
12	zoning data storage for storing configuration data indicative of the zone
13	configuration of the fabric;
14	a comparison circuit connected to said zoning data storage for comparing
15	at least a portion of the initial fields of a received data packet with said stored
16	configuration data and providing an output; and
17	an action circuit connected to said comparison circuit and utilizing said
18	comparison circuit output to determine an action to be performed on the received data
19	packet,
20	wherein the action determined by said action circuit is to provide a data packet to
21	said second transmitting port for transmission of the received data packet to the external

data packet processing device.

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- 1 2. (Currently Amended) The Fibre Channel device of claim 1, wherein an additional
- 2 action determined by said action circuit is to forward the a data packet, and wherein said
- 3 first transmitting port transmits the received data packet.
- 1 3. (Currently Amended) The Fibre Channel device of claim 1, wherein an additional
- 2 action determined by said action circuit is to discard the a data packet, and wherein said
- 3 first transmitting port does not transmit the received data packet.
- 1 4. (Original) The Fibre Channel device of claim 1, wherein said device logic further
- 2 includes:
- 3 a memory for storing data packets;
- 4 receiver logic connected to said receiving port and said memory for receiving a
- 5 data packet from said receiving port and storing the data packet in said memory; and
- 6 transmitter logic connected to said first and second transmitting ports and said
- 7 memory for retrieving the data packet from said memory and providing the data packet to
- 8 said first or second transmitting port.
- 1 5. (Currently Amended) The Fibre Channel device of claim 1, wherein the plurality
- 2 of external data devices are fabric-attached, loop-attached or a combination of fabric-
- 3 attached and loop-attached.
- 1 6. (Currently Amended) The Fibre Channel device of claim 1, wherein the plurality
- 2 of external data devices are fabric-attached.
- 1 7. (Currently Amended) The Fibre Channel device of claim 1, wherein said at least a
- 2 portion of the initial fields compared by said comparison circuit include the portion for at
- 3 least one of the a source address, a value relating to the a destination address, the a Fibre
- 4 Channel type and the a logical unit number (LUN) value.

- 1 8. (Original) The Fibre Channel device of claim 7, wherein said at least a portion of the
- 2 initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 9. (Original) The Fibre Channel device of claim 8, wherein said at least a portion of the
- 2 initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 10. (Original) The Fibre Channel device of claim 9, wherein said at least a portion of the
- 2 initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 11. (Currently Amended) The Fibre Channel device of claim 1, wherein said first and
- 2 second transmitting ports have port numbers, wherein said action circuit uses a port
- 3 number value to provide the received data packet to said second transmitting port, and
- 4 wherein said port number value used by said action circuit is programmable.
- 1 12. (Currently Amended) A Fibre Channel switch for use in a Fibre Channel fabric,
- 2 the fabric coupling a plurality of external data devices, the fabric configured into at least
- 3 two zones, where the external devices are allowed to exchange data packets only with
- 4 external data devices in the same zone, the Fibre Channel switch enforcing the at least
- 5 two zones in hardware, the Fibre Channel switch comprising:
- 6 a microprocessor;
- 7 local memory connected to said microprocessor; and
- 8 a Fibre Channel device connected to and controlled by said microprocessor,
- 9 wherein said Fibre Channel device includes:
- 10 a receiving port for coupling to the fabric and receiving data packets;

11	a first transmitting port for coupling to the fabric and transmitting data
12	packets;
13	a second transmitting port for coupling to an external data packet
14	processing device; and
15	device logic connecting said receiving port and said first and second
16	transmitting ports, wherein said device logic includes:
17	zoning data storage for storing configuration data indicative of the
18	zone configuration of the fabric;
19	a comparison circuit connected to said zoning data storage for
20	comparing at least a portion of the initial fields of a received data packet with said stored
21	configuration data and providing an output; and
22	an action circuit connected to said comparison circuit and utilizing
23	said comparison circuit output to determine an action to be performed on the received
24	data packet,
25	wherein the action determined by said action circuit is to provide a data packet to
26	said second transmitting port for transmission of the $\underline{\text{received}}$ data packet to the external
27	data packet processing device.
1	13. (Currently Amended) The Fibre Channel switch of claim 12, wherein an
2	additional action determined by said action circuit is to forward the a data packet, and
3	wherein said first transmitting port transmits the received data packet.
1	14. (Currently Amended) The Fibre Channel switch of claim 12, wherein an
2	additional action determined by said action circuit is to discard the a_data packet, and
3	wherein said first transmitting port does not transmit the received data packet.
1	15. (Original) The Fibre Channel switch of claim 12, wherein said device logic further
2	includes:
3	a memory for storing data packets;

- 4 receiver logic connected to said receiving port and said memory for receiving a
- 5 data packet from said receiving port and storing the data packet in said memory; and
- 6 transmitter logic connected to said first and second transmitting ports and said
- 7 memory for retrieving the data packet from said memory and providing the data packet to
- 8 said first or second transmitting port.
- 1 16. (Currently Amended) The Fibre Channel switch of claim 12, wherein the plurality
- 2 of external data devices are fabric-attached, loop-attached or a combination of fabric-
- 3 attached and loop-attached.
- 1 17. (Currently Amended) The Fibre Channel switch of claim 12, wherein the plurality
- 2 of external data devices are fabric-attached.
- 1 18. (Currently Amended) The Fibre Channel switch of claim 12, wherein said at least
- 2 a portion of the initial fields compared by said comparison circuit include the portion for
- 3 at least one of the a source address, a value relating to the a destination address, the a
- 4 Fibre Channel type and the-a logical unit number (LUN) value.
- 1 19. (Original) The Fibre Channel switch of claim 18, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 20. (Original) The Fibre Channel switch of claim 19, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 21. (Original) The Fibre Channel switch of claim 20, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one

3 more of the source address, a value relating to the destination address, the Fibre Channel type and the logical unit number (LUN) value. 4 1 22. (Currently Amended) The Fibre Channel switch of claim 12, wherein said first 2 and second transmitting ports have port numbers, wherein said action circuit uses a port number value to provide the received data packet to said second transmitting port, and 3 4 wherein said port number value used by said action circuit is programmable. 1 23. (Currently Amended) A Fibre Channel fabric comprising: 2 a plurality of external data devices; 3 a first Fibre Channel switch coupled to a first portion of said plurality of external 4 data devices; and 5 a second Fibre Channel switch coupled to a second portion of said plurality of 6 data external devices and coupled to said first Fibre Channel switch, 7 wherein the fabric is configured into at least two zones, where said-external data 8 devices are allowed to exchange data packets only with external data devices in the same 9 zone and wherein said first and second Fibre Channel switches enforce the at least two 10 zones in hardware, each of said first and second Fibre Channel switches including: 11 a microprocessor; 12 local memory connected to said microprocessor, and 13 a Fibre Channel device connected to and controlled by said 14 microprocessor, wherein said Fibre Channel device includes: 15 a receiving port for coupling to the fabric and receiving data 16 packets: 17 a first transmitting port for coupling to the fabric and transmitting 18 data packets; 19 a second transmitting port for coupling to an external data packet 20 processing device; and 21 device logic connecting said receiving port and said first and second transmitting ports, wherein said device logic includes: 22

23	zoning data storage for storing configuration data indicative
24	of the zone configuration of the fabric;
25	a comparison circuit connected to said zoning data storage
26	for comparing at least a portion of the initial fields of a received data packet with said
27	stored configuration data and providing an output; and
28	an action circuit connected to said comparison circuit and
29	utilizing said comparison circuit output to determine an action to be performed on the
30	received data packet,
31	wherein the action determined by said action circuit is to provide a data packet to
32	said second transmitting port for transmission of the $\underline{\text{received}}$ data packet to the external
33	data packet processing device.
1	24. (Currently Amended) The Fibre Channel fabric of claim 23, wherein an
2	additional action determined by said action circuit is to forward the a data packet, and
3	wherein said first transmitting port transmits the received data packet.
1	25. (Currently Amended) The Fibre Channel fabric of claim 23, wherein an
2	additional action determined by said action circuit is to discard the a data packet, and
3	wherein said first transmitting port does not transmit the received data packet.
1	26. (Original) The Fibre Channel fabric of claim 23, wherein said device logic further
2	includes:
3	a memory for storing data packets;
4	receiver logic connected to said receiving port and said memory for receiving a
5	data packet from said receiving port and storing the data packet in said memory; and
6	transmitter logic connected to said first and second transmitting ports and said
7	memory for retrieving the data packet from said memory and providing the data packet to
8	said first or second transmitting port.

- 1 27. (Currently Amended) The Fibre Channel fabric of claim 23, wherein said
- 2 plurality of external data devices are fabric-attached, loop-attached or a combination of
- 3 fabric-attached and loop-attached.
- 1 28. (Currently Amended) The Fibre Channel fabric of claim 23, wherein said
- 2 plurality of external data devices are fabric-attached.
- 1 29. (Currently Amended) The Fibre Channel fabric of claim 23. wherein said at least
- 2 a portion of the initial fields compared by said comparison circuit include the portion for
- 3 at least one of the a source address, a value relating to the a destination address, the a
- 4 Fibre Channel type and the-a logical unit number (LUN) value.
- 1 30. (Original) The Fibre Channel device of claim 29, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 31. (Original) The Fibre Channel device of claim 30, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 32. (Original) The Fibre Channel device of claim 31, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 33. (Currently Amended) The Fibre Channel device of claim 23, wherein said first
- 2 and second transmitting ports have port numbers, wherein said action circuit uses a port

3 number value to provide the received data packet to said second transmitting port, and 4 wherein said port number value used by said action circuit is programmable. 1 34. (Currently Amended) A Fibre Channel device for use in a Fibre Channel fabric, 2 the fabric coupling a plurality of external data devices, the fabric configured into at least 3 two zones, where the external data devices are allowed to exchange data packets only 4 with external data devices in the same zone, the Fibre Channel device enforcing the at 5 least two zones in hardware, the Fibre Channel device comprising: 6 a receiving port for coupling to the fabric and receiving data packets; 7 a first transmitting port for coupling to the fabric and transmitting data packets; 8 a second transmitting port for coupling to an external data packet processing 9 device: and 10 device logic connecting said receiving port and said first and second transmitting 11 ports, wherein said device logic includes: 12 zoning data storage for storing configuration data indicative of the zone 13 configuration of the fabric: 14 a comparison circuit connected to said zoning data storage for comparing 15 at least a portion of the initial fields of a received data packet with said stored 16 configuration data and providing an output; and 17 an action circuit connected to said comparison circuit and utilizing said 18 comparison circuit output to determine an action to be performed on the received data 19 nacket. 20 wherein the action determined by said action circuit is to provide a data packet to 21 said second transmitting port for transmission of the received data packet to the external 22 data packet processing device, and 23 wherein said zoning data storage includes: 24 a data packet register for storing portions of a data packet: 25 a first memory storing filtering information relating to a first portion of a 26 data packet;

27	a first annual to said first arranged and data arranged
27	a first comparator coupled to said first memory and said data packet
28	register comparing said information to the data packet and providing an output indicative
29	thereof;
30	a second memory storing filtering information relating to a second portion
31	of the data packet;
32	a second comparator coupled to said second memory and said data packet
33	register comparing said information to the data packet and providing an output indicative
34	thereof;
35	a third memory coupled to said first comparator indicating group
36	information based on said first comparator output; and
37	a fourth memory coupled to said second comparator indicating group
38	information based on said second comparator output.
1	35. (Original) The Fibre Channel device of claim 34, wherein said first memory and said
2	first comparator form a content addressable memory; and
3	said second memory and said second comparator form a content
4	addressable memory.
1	36. (Currently Amended) The Fibre Channel device of claim 34, wherein an
2	additional action determined by said action circuit is to forward the a data packet, and
3	wherein said first transmitting port transmits the received data packet.
1	37. (Currently Amended) The Fibre Channel device of claim 34, wherein an
2	additional action determined by said action circuit is to discard the a data packet, and
3	wherein said first transmitting port does not transmit the received data packet.
1	38. (Original) The Fibre Channel device of claim 34, wherein said device logic further
2	includes:
3	a memory for storing data packets;

- 4 receiver logic connected to said receiving port and said memory for receiving a
 5 data packet from said receiving port and storing the data packet in said memory; and
 6 transmitter logic connected to said first and second transmitting ports and said
- memory for retrieving the data packet from said memory and providing the data packet to said first or second transmitting port.
- 1 39. (Currently Amended) The Fibre Channel device of claim 34, wherein said at least
- 2 a portion of the initial fields compared by said comparison circuit include the portion for
- 3 at least one of the a source address, a value relating to the a destination address, the a
- 4 Fibre Channel type and the-a logical unit number (LUN) value.
- 40. (Original) The Fibre Channel device of claim 39, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 41. (Original) The Fibre Channel device of claim 40, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 42. (Original) The Fibre Channel device of claim 41, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 43. (Currently Amended) The Fibre Channel device of claim 34, wherein said first
- 2 and second transmitting ports have port numbers, wherein said action circuit uses a port
- 3 number value to provide the received data packet to said second transmitting port, and
- 4 wherein said port number value used by said action circuit is programmable.

1	44. (Currently Amended) A Fibre Channel switch for use in a Fibre Channel fabric,
2	the fabric coupling a plurality of external data devices, the fabric configured into at least
3	two zones, where the external devices are allowed to exchange data packets only with
4	external data devices in the same zone, the Fibre Channel switch enforcing the at least
5	two zones in hardware, the Fibre Channel switch comprising:
6	a microprocessor;
7	local memory connected to said microprocessor, and
8	a Fibre Channel device connected to and controlled by said microprocessor,
9	wherein said Fibre Channel device includes:
10	a receiving port for coupling to the fabric and receiving data packets;
11	a first transmitting port for coupling to the fabric and transmitting data
12	packets;
13	a second transmitting port for coupling to an external data packet
14	processing device; and
15	device logic connecting said receiving port and said first and second
16	transmitting ports, wherein said device logic includes:
17	zoning data storage for storing configuration data indicative of the
18	zone configuration of the fabric;
19	a comparison circuit connected to said zoning data storage for
20	comparing at least a portion of the initial fields of a received data packet with said stored
21	configuration data and providing an output; and
22	an action circuit connected to said comparison circuit and utilizing
23	said comparison circuit output to determine an action to be performed on the received
24	data packet,
25	wherein the action determined by said action circuit is to provide a data packet to
26	said second transmitting port for transmission of the $\underline{\text{received}}$ data packet to the external
27	data packet processing device, and
28	wherein said zoning data storage includes:
29	a data packet register for storing portions of a data packet;

30 a first memory storing filtering information relating to a first portion of a 31 data packet: 32 a first comparator coupled to said first memory and said data packet 33 register comparing said information to the data packet and providing an output indicative 34 thereof: 35 a second memory storing filtering information relating to a second portion 36 of the data packet: 37 a second comparator coupled to said second memory and said data packet 38 register comparing said information to the data packet and providing an output indicative 39 thereof: 40 a third memory coupled to said first comparator indicating group 41 information based on said first comparator output; and 42 a fourth memory coupled to said second comparator indicating group 43 information based on said second comparator output. 1 45. (Original) The Fibre Channel device of claim 44, wherein said first memory and said 2 first comparator form a content addressable memory; and 3 said second memory and said second comparator form a content 4 addressable memory. The Fibre Channel switch of claim 44, wherein an 1 46. (Currently Amended) 2 additional action determined by said action circuit is to forward thea data packet, and 3 wherein said first transmitting port transmits the received data packet. 1 47. (Currently Amended) The Fibre Channel switch of claim 44, wherein an 2 additional action determined by said action circuit is to discard the a data packet, and 3 wherein said first transmitting port does not transmit the received data packet. 1 48. (Original) The Fibre Channel switch of claim 442, wherein said device logic further 2 includes:

3 a memory for storing data packets;

4 receiver logic connected to said receiving port and said memory for receiving a

5 data packet from said receiving port and storing the data packet in said memory; and

6 transmitter logic connected to said first and second transmitting ports and said

7 memory for retrieving the data packet from said memory and providing the data packet to

8 said first or second transmitting port.

- 1 49. (Currently Amended) The Fibre Channel switch of claim 44, wherein said at least
- 2 a portion of the initial fields compared by said comparison circuit include the portion for
- 3 at least one of the a source address, a value relating to the a destination address, the a
- 4 Fibre Channel type and the-a logical unit number (LUN) value.
- 1 50. (Original) The Fibre Channel switch of claim 49, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 51. (Original) The Fibre Channel switch of claim 50, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 52. (Original) The Fibre Channel switch of claim 51, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 53. (Currently Amended) The Fibre Channel switch of claim 44, wherein said first
- 2 and second transmitting ports have port numbers, wherein said action circuit uses a port

3	number value to provide the received data packet to said second transmitting port, and
4	wherein said port number value used by said action circuit is programmable.
1	54. (Currently Amended) A Fibre Channel fabric comprising:
2	a plurality of external data devices;
3	a first Fibre Channel switch coupled to a first portion of said plurality of external
4	data devices; and
5	a second Fibre Channel switch coupled to a second portion of said plurality of
6	data external devices and coupled to said first Fibre Channel switch,
7	wherein the fabric is configured into at least two zones, where said-external data
8	devices are allowed to exchange data packets only with external data devices in the same
9	zone and wherein said first and second Fibre Channel switches enforce the at least two
10	zones in hardware, each of said first and second Fibre Channel switches including:
11	a microprocessor;
12	local memory connected to said microprocessor; and
13	a Fibre Channel device connected to and controlled by said
14	microprocessor, wherein said Fibre Channel device includes:
15	a receiving port for coupling to the fabric and receiving data
16	packets;
17	a first transmitting port for coupling to the fabric and transmitting
18	data packets;
19	a second transmitting port for coupling to an external data packet
20	processing device; and
21	device logic connecting said receiving port and said first and
22	second transmitting ports, wherein said device logic includes:
23	zoning data storage for storing configuration data indicative
24	of the zone configuration of the fabric;
25	a comparison circuit connected to said zoning data storage
26	for comparing at least a portion of the initial fields of a received data packet with said

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stored configuration data and providing an output; and

28	an action circuit connected to said comparison circuit and
29	utilizing said comparison circuit output to determine an action to be performed on the
30	received data packet,
31	wherein the action determined by said action circuit is to provide a data packet to
32	said second transmitting port for transmission of the received data packet to the external
33	data packet processing device, and
34	wherein said zoning data storage includes:
35	a data packet register for storing portions of a data packet;
36	a first memory storing filtering information relating to a first portion of a
37	data packet;
38	a first comparator coupled to said first memory and said data packet
39	register comparing said information to the data packet and providing an output indicative
40	thereof;
41	a second memory storing filtering information relating to a second portion
42	of the data packet;
43	a second comparator coupled to said second memory and said data packet
44	register comparing said information to the data packet and providing an output indicative
45	thereof;
46	a third memory coupled to said first comparator indicating group
47	information based on said first comparator output; and
48	a fourth memory coupled to said second comparator indicating group
49	information based on said second comparator output.
1	55. (Original) The Fibre Channel device of claim 54, wherein said first memory and said
2	first comparator form a content addressable memory; and
3	said second memory and said second comparator form a content
4	addressable memory.
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- 6 56. (Currently Amended) The Fibre Channel fabric of claim 54, wherein an
- 7 additional action determined by said action circuit is to forward the-a data packet, and
- 8 wherein said first transmitting port transmits the received data packet.
- 1 57. (Currently Amended) The Fibre Channel fabric of claim 54, wherein an
- 2 additional action determined by said action circuit is to discard the a data packet, and
- 3 wherein said first transmitting port does not transmit the received data packet.
- 1 58. (Original) The Fibre Channel fabric of claim 54, wherein said device logic further
- 2 includes:
- 3 a memory for storing data packets;
- 4 receiver logic connected to said receiving port and said memory for receiving a
- 5 data packet from said receiving port and storing the data packet in said memory; and
- 6 transmitter logic connected to said first and second transmitting ports and said
- 7 memory for retrieving the data packet from said memory and providing the data packet to
- 8 said first or second transmitting port.
- 1 59. (Currently Amended) The Fibre Channel fabric of claim 54, wherein said at least
- 2 a portion of the initial fields compared by said comparison circuit include the portion for
- 3 at least one of the a source address, a value relating to the a destination address, the a
- 4 Fibre Channel type and the a logical unit number (LUN) value.
- 1 60. (Original) The Fibre Channel device of claim 59, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 61. (Original) The Fibre Channel device of claim 60, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one

- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 62. (Original) The Fibre Channel device of claim 61, wherein said at least a portion of
- 2 the initial fields compared by said comparison circuit include the portion for at least one
- 3 more of the source address, a value relating to the destination address, the Fibre Channel
- 4 type and the logical unit number (LUN) value.
- 1 63. (Currently Amended) The Fibre Channel device of claim 54, wherein said first
- 2 and second transmitting ports have port numbers, wherein said action circuit uses a port
- 3 number value to provide the received data packet to said second transmitting port, and
- 4 wherein said port number value used by said action circuit is programmable.